

10/1/03

Form PTO-1449 (Rev. 2-83)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 03-0717	SERIAL NO. Not Yet Assigned 10/676602
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)		APPLICANTS: Jau-Wen Chen et al.	
		FILING DATE: October 1, 2003	GROUP Not Yet Assigned

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
Mew		6 4 6 9 3 5 4	10/22/02	Hirata			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION	
							Y	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

Mew		"ESD in Silicon Integrated Circuits", Design Concepts, Chapter 4, Section 2, p 69-71, pre-2003.
		Duvvury, C., "ESD On-Chip Protection in Advanced Technologies", 1999 ESD tutorial, Orlando, Florida, September 26, 1999.
Mew		Duvvury, C et al., "Advanced CMOS Protection Device Trigger Mechanisms During CDM", EOS/ESD Symposium, 1995.

EXAMINER <i>Matthew E. Warner</i>	DATE CONSIDERED 3/2/05
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***EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.